

6.002

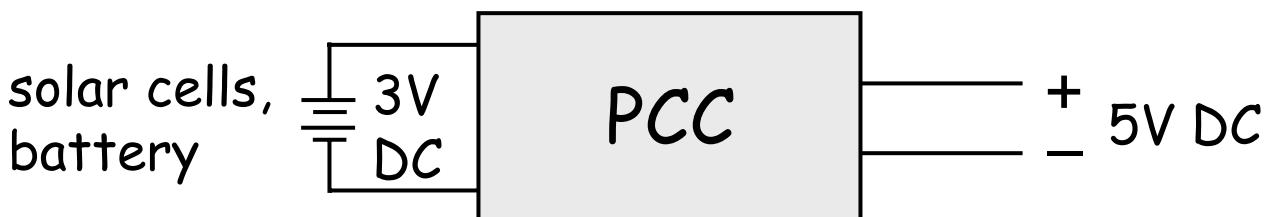
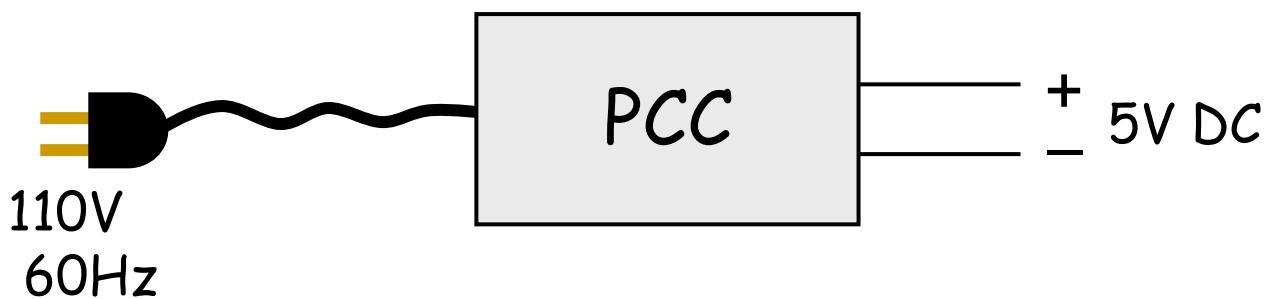
**CIRCUITS AND
ELECTRONICS**

Power Conversion Circuits and Diodes

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6.002 Fall 2000 Lecture 24

Power Conversion Circuits (PCC)



DC-to-DC UP converter

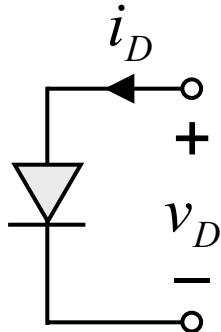
Power efficiency of converter important,
so use lots of devices:

MOSFET switches, clock circuits,
inductors, capacitors, op amps, diodes



Reading: Chapter 16 and 4.4 of A & L.

First, let's look at the diode



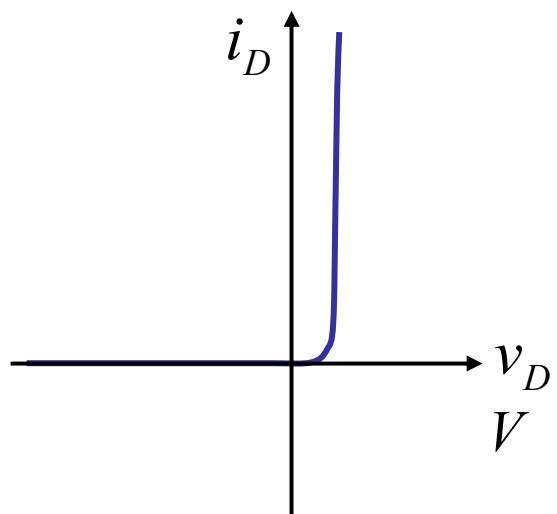
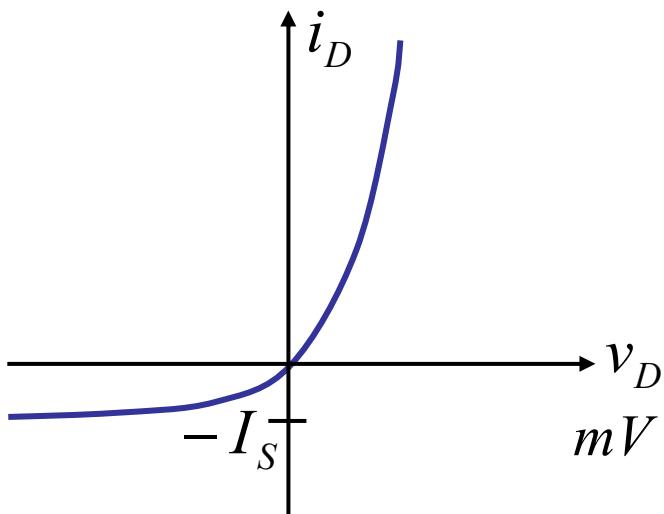
$$i_D = I_S \left(e^{\frac{v_D}{V_T}} - 1 \right)$$

$$I_S = 10^{-12} A$$

$$V_T = 0.025V$$

$$V_T = \frac{kT}{q}$$

Boltzmann's constant
temperature in Kelvins
charge of an electron



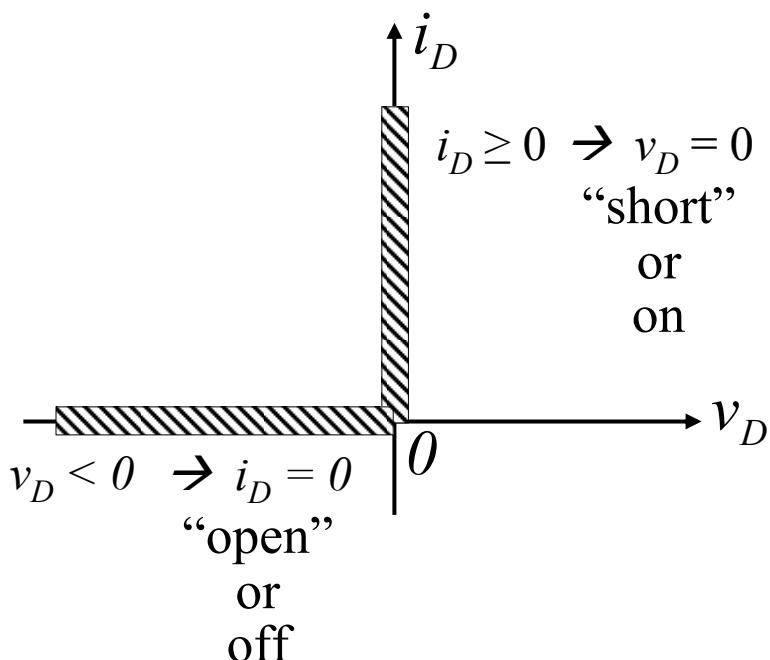
Can use this exponential model with analysis methods learned earlier

- analytical
- graphical
- incremental

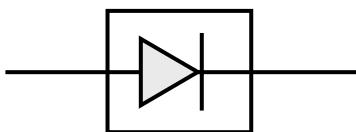
(Our fake expodweeb was modeled after this device!)

Another analysis method: piecewise-linear analysis

P-L diode models:

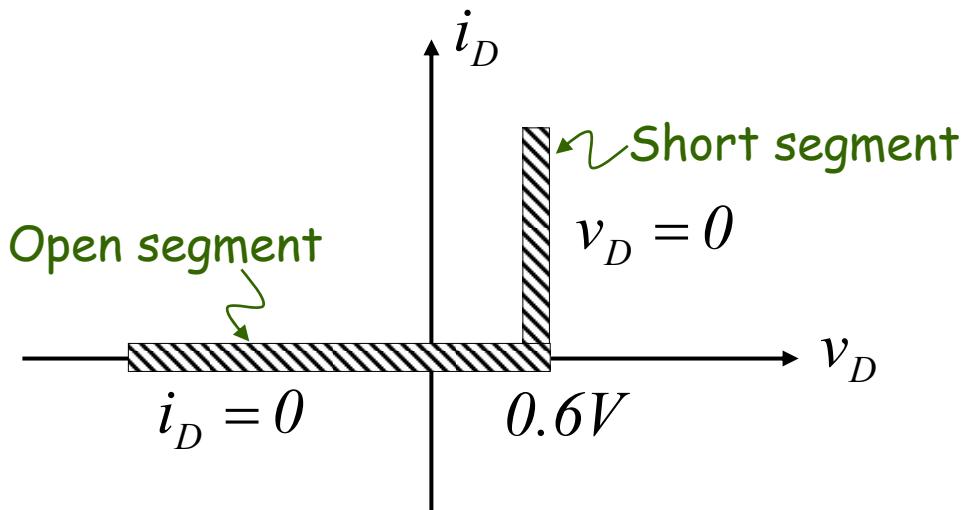
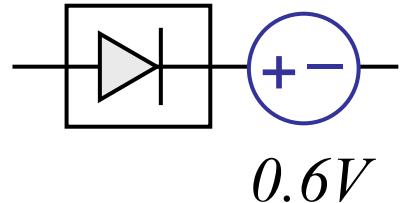


Ideal diode model



Another analysis method: piecewise-linear analysis

"Practical" diode model
ideal with offset



Another analysis method: piecewise-linear analysis

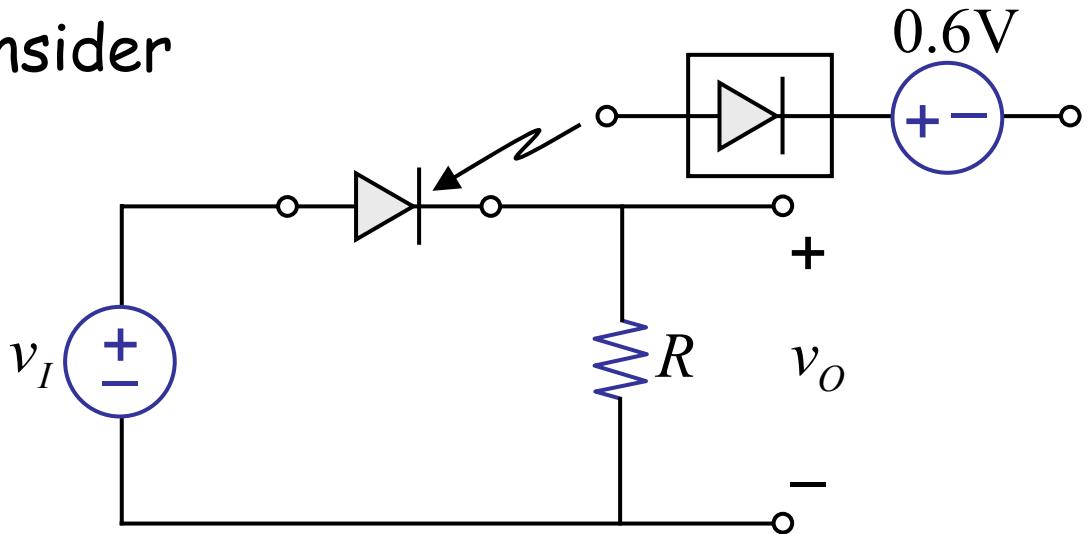
Piecewise-linear analysis method

- Replace nonlinear characteristic with linear segments.
- Perform linear analysis within each segment.

Example

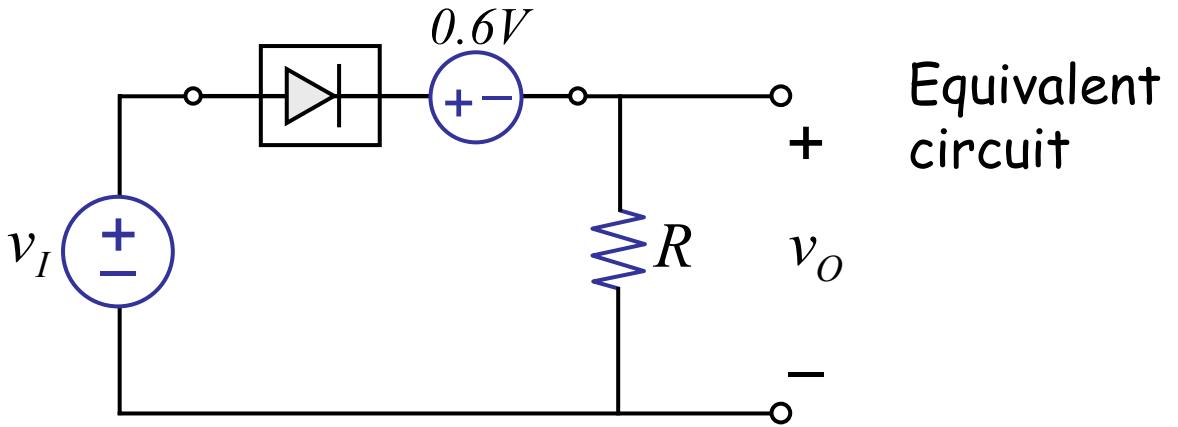
(We will build up towards an AC-to-DC converter)

Consider

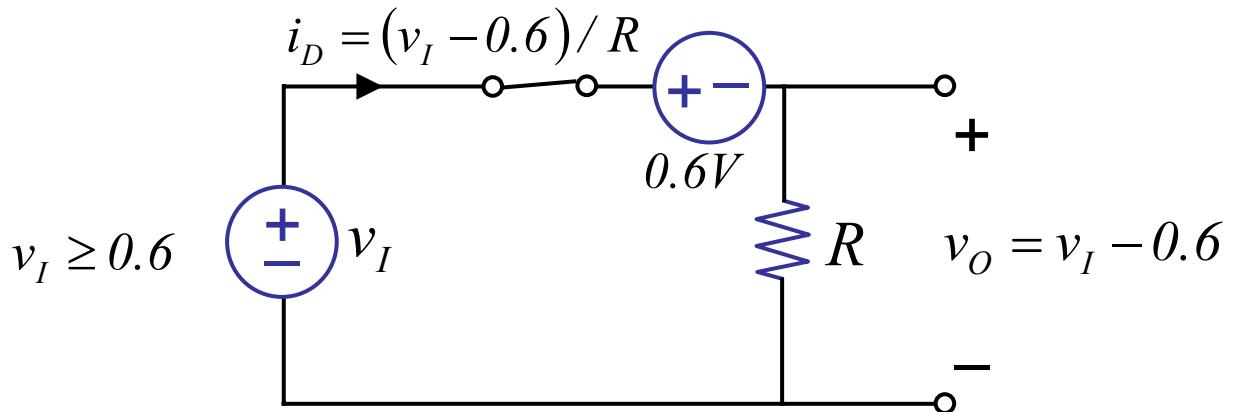


v_I is a sine wave

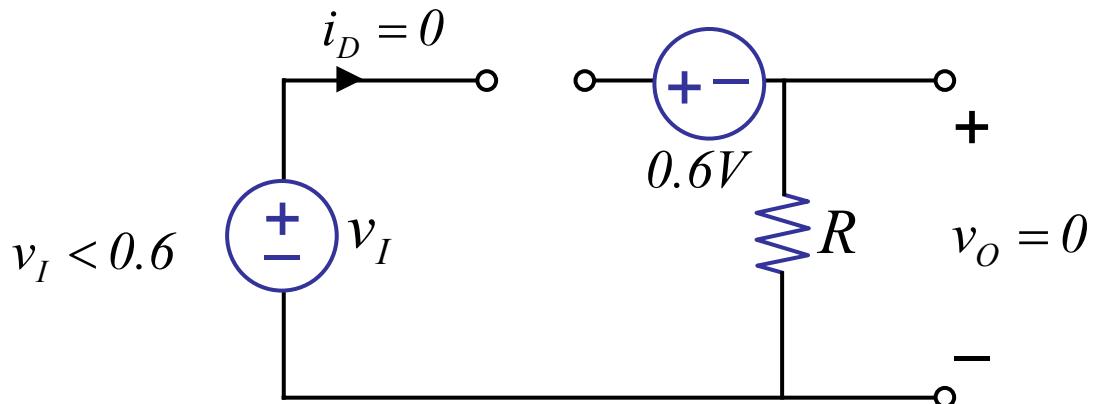
Example



"Short segment":

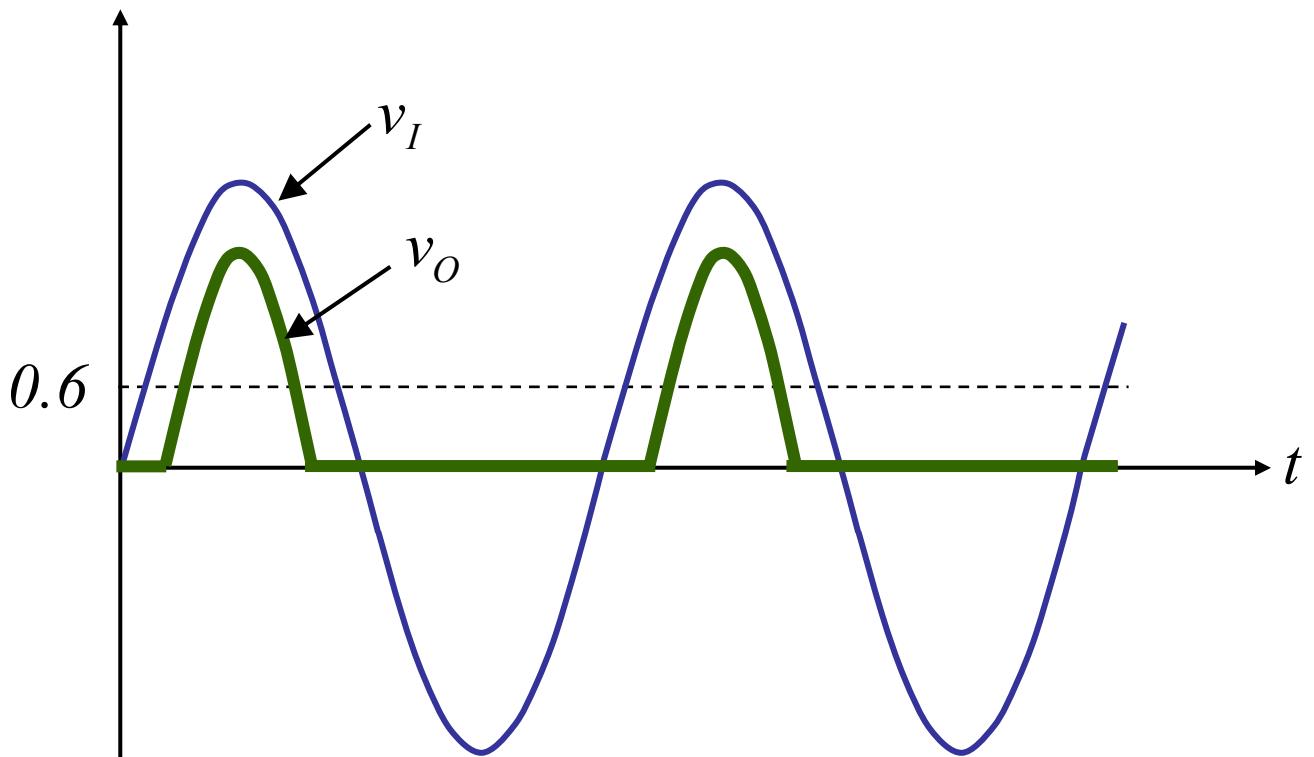


"Open segment":



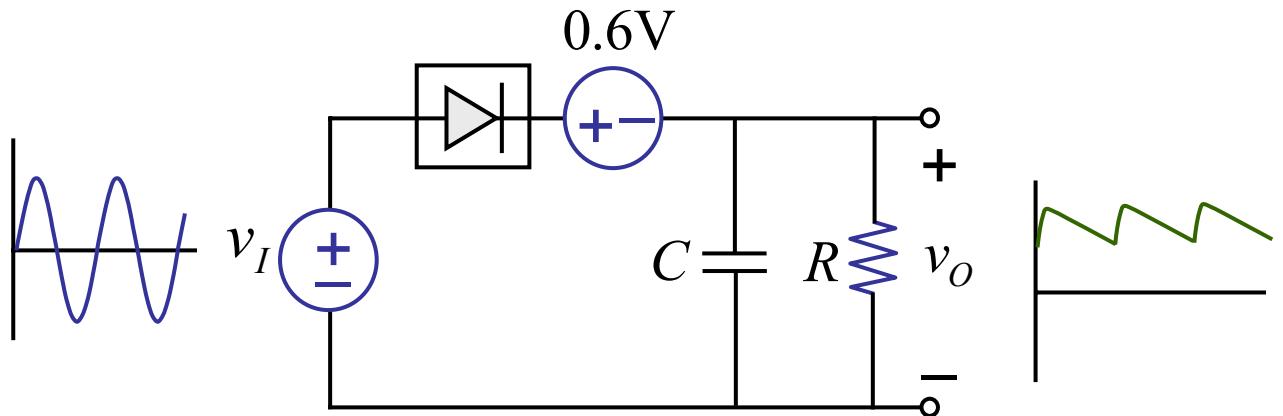
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Example

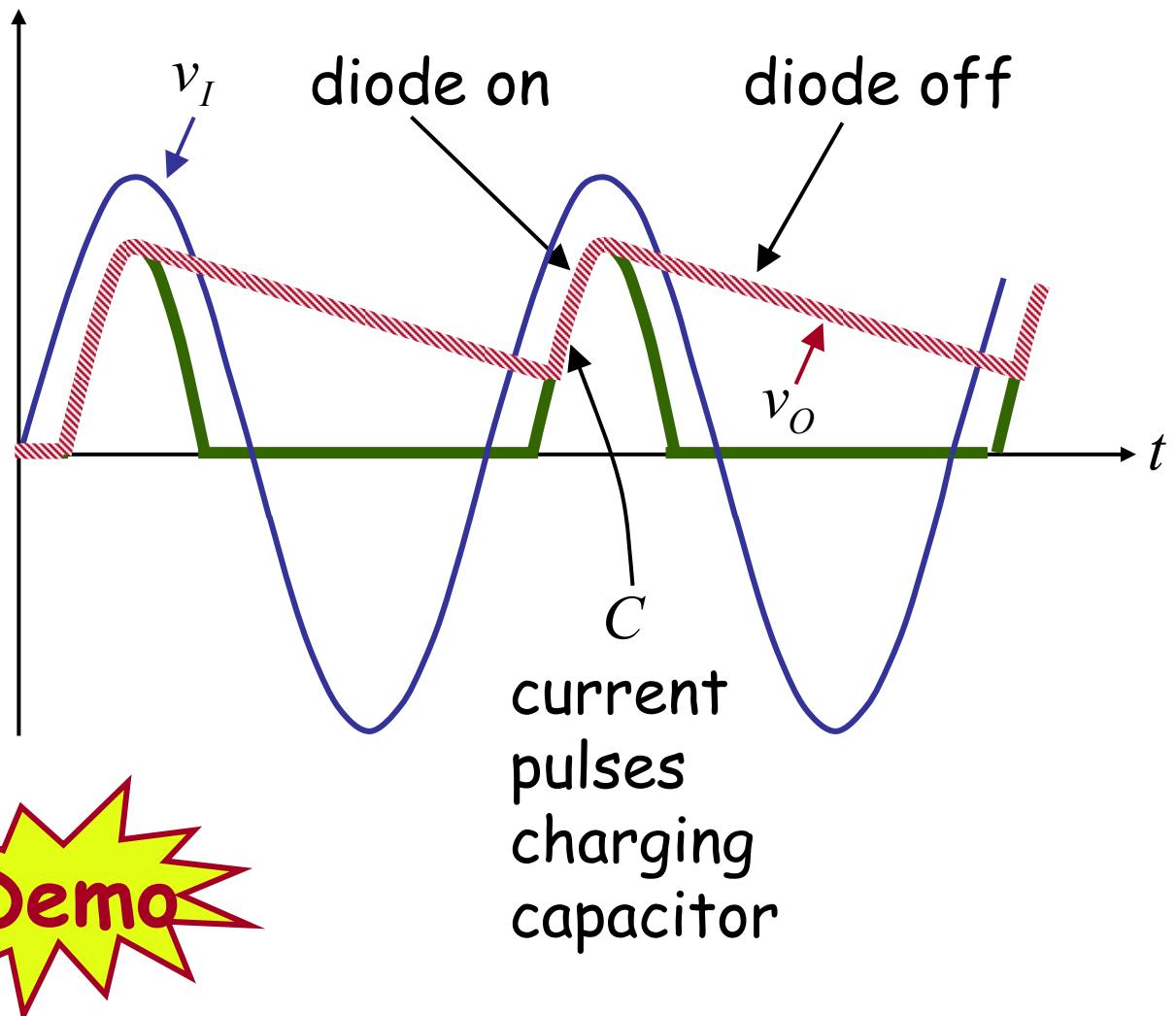


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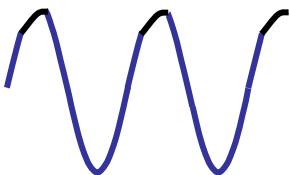
Now consider – a half-wave rectifier



A half-wave rectifier

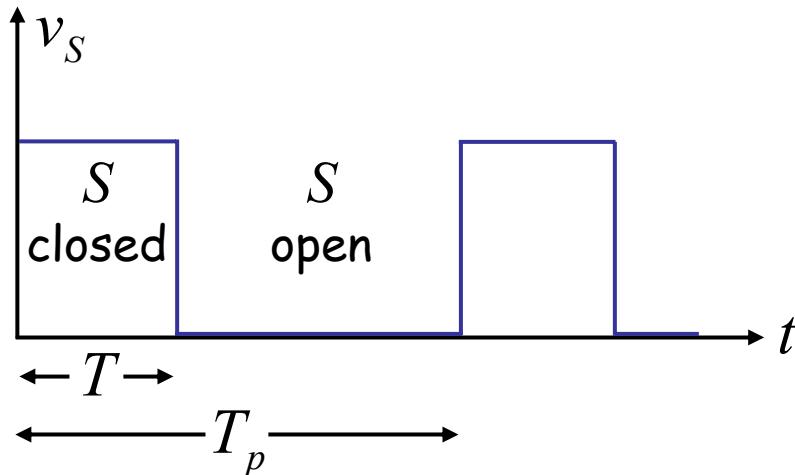
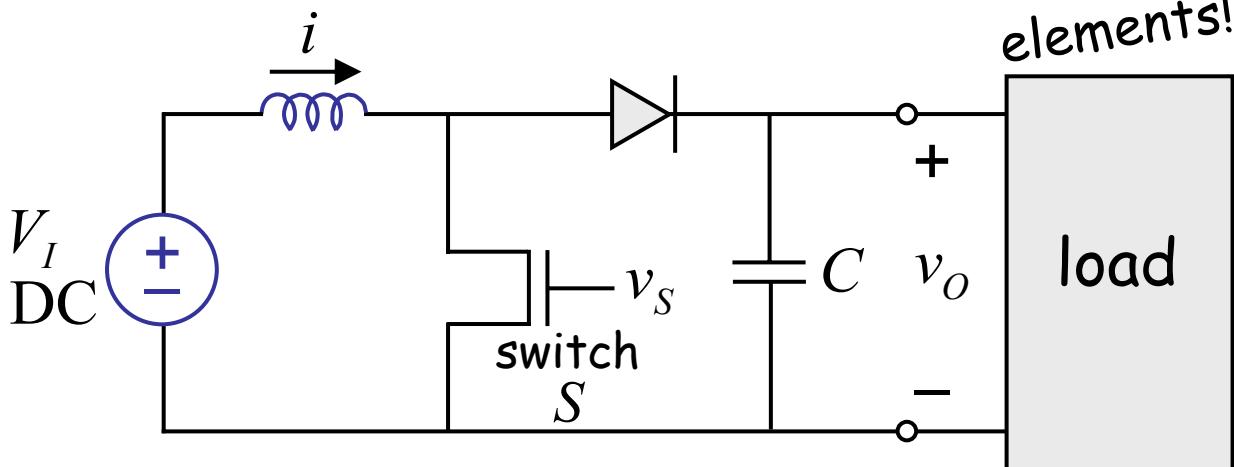


MIT's supply shows
“snipping” at the peaks
(because current drawn
at the peaks)



DC-to-DC UP Converter

Do not use resistive elements!



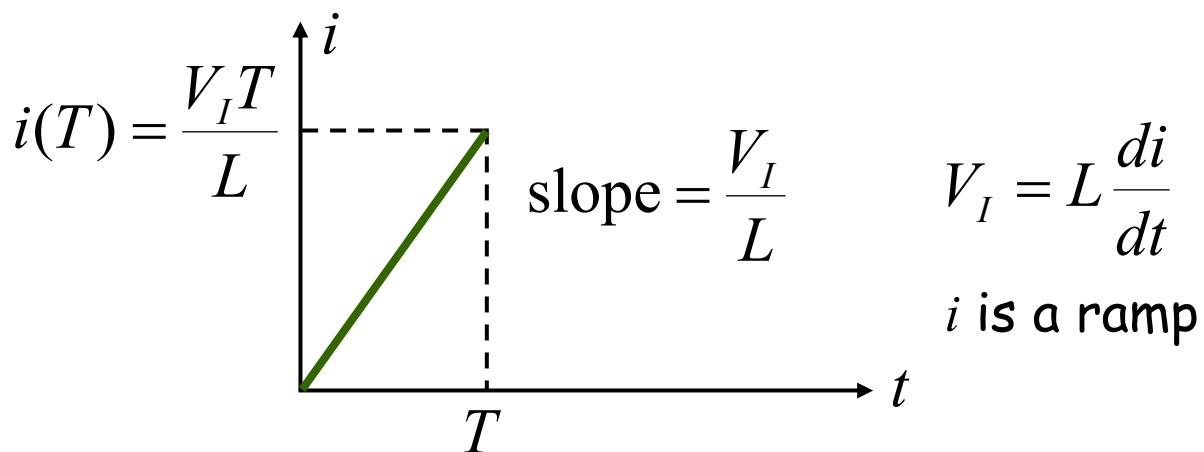
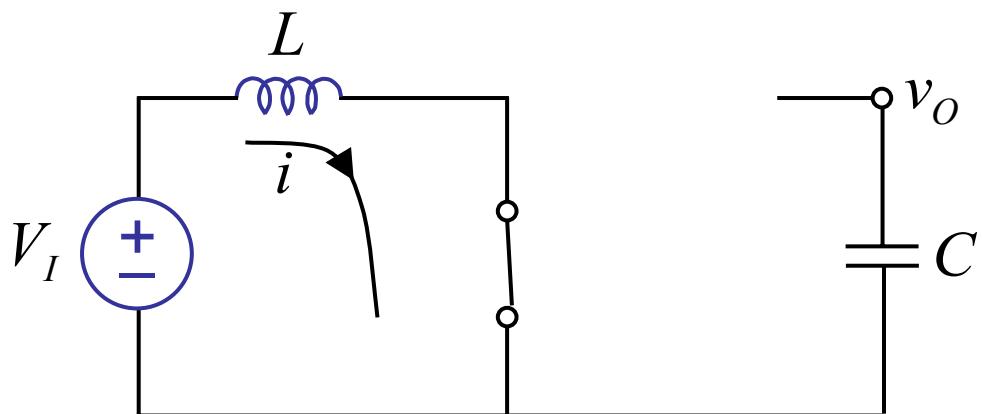
The circuit has 3 states:

- I. S is on, diode is off
 i increases linearly
- II. S turns off, diode turns on
 C charges up, v_O increases
- III. S is off, diode turns off
 C holds v_O (discharges into load)

More detailed analysis

I. Assume $i(0) = 0, v_O(0) > 0$

S on at $t = 0$, diode off

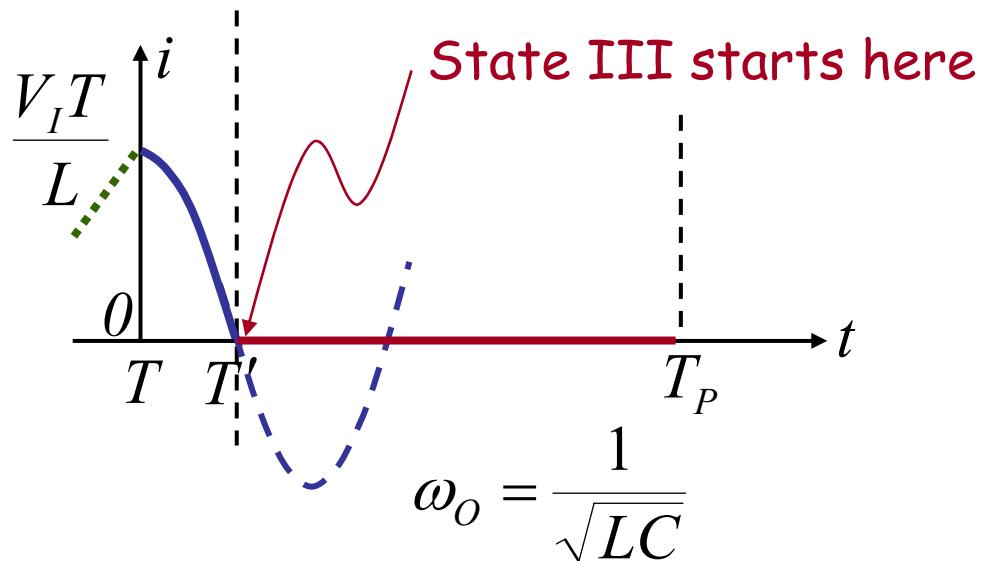
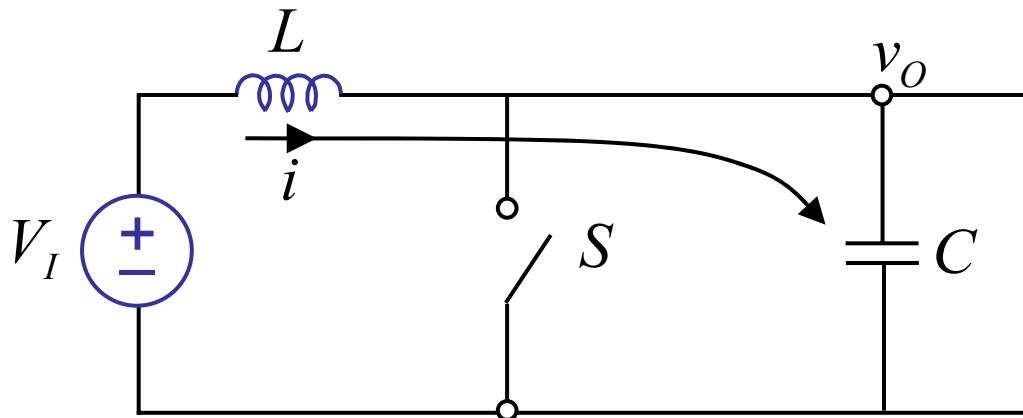


$$\Delta E = \text{energy stored at } t = T : \frac{1}{2} L i(T)^2$$

$$\Delta E = \frac{V_I^2 T^2}{2L}$$

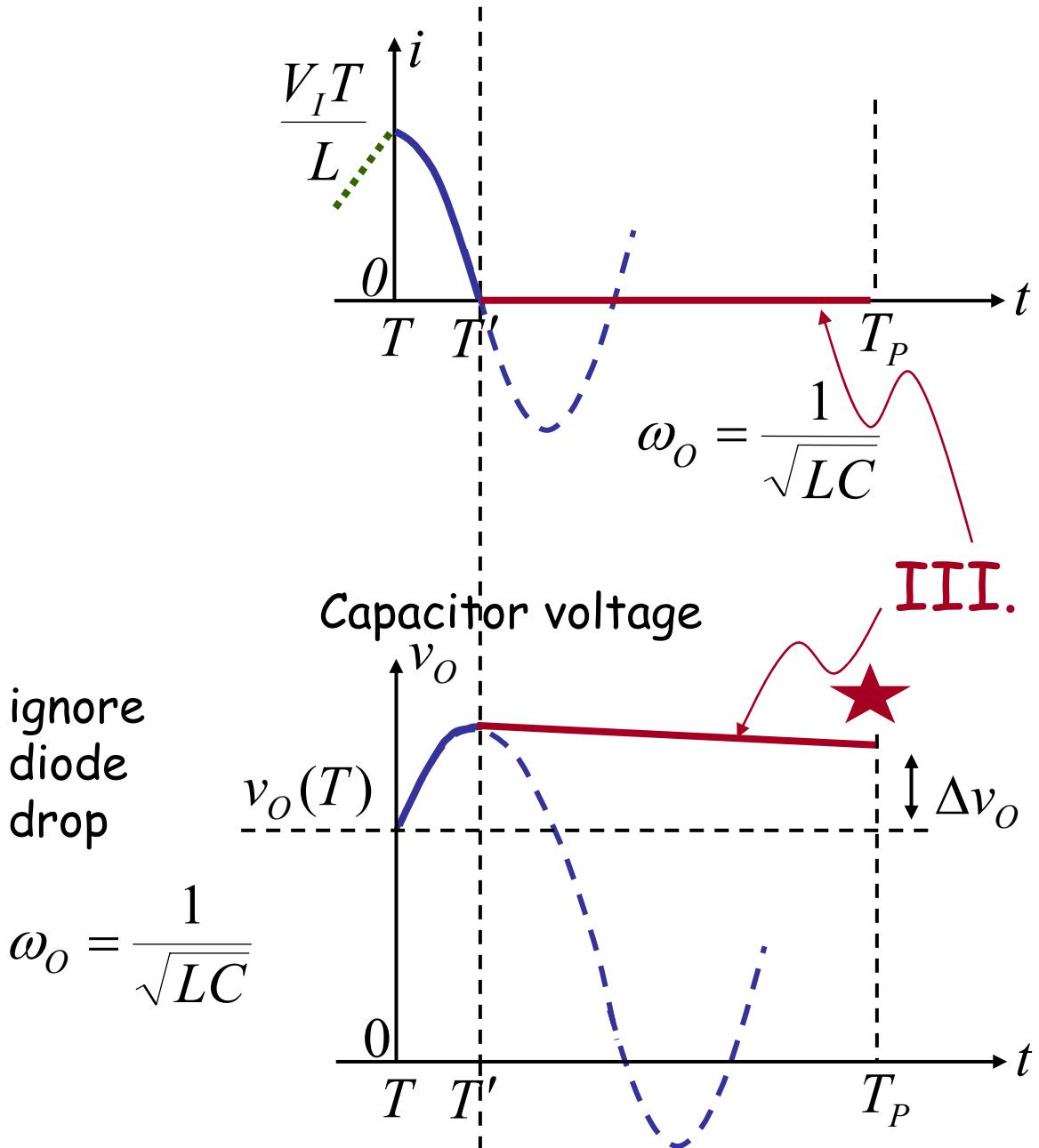
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II. S turns off at $t = T$ diode turns on (ignore diode voltage drop)



Diode turns off at T' when i tries to go negative.

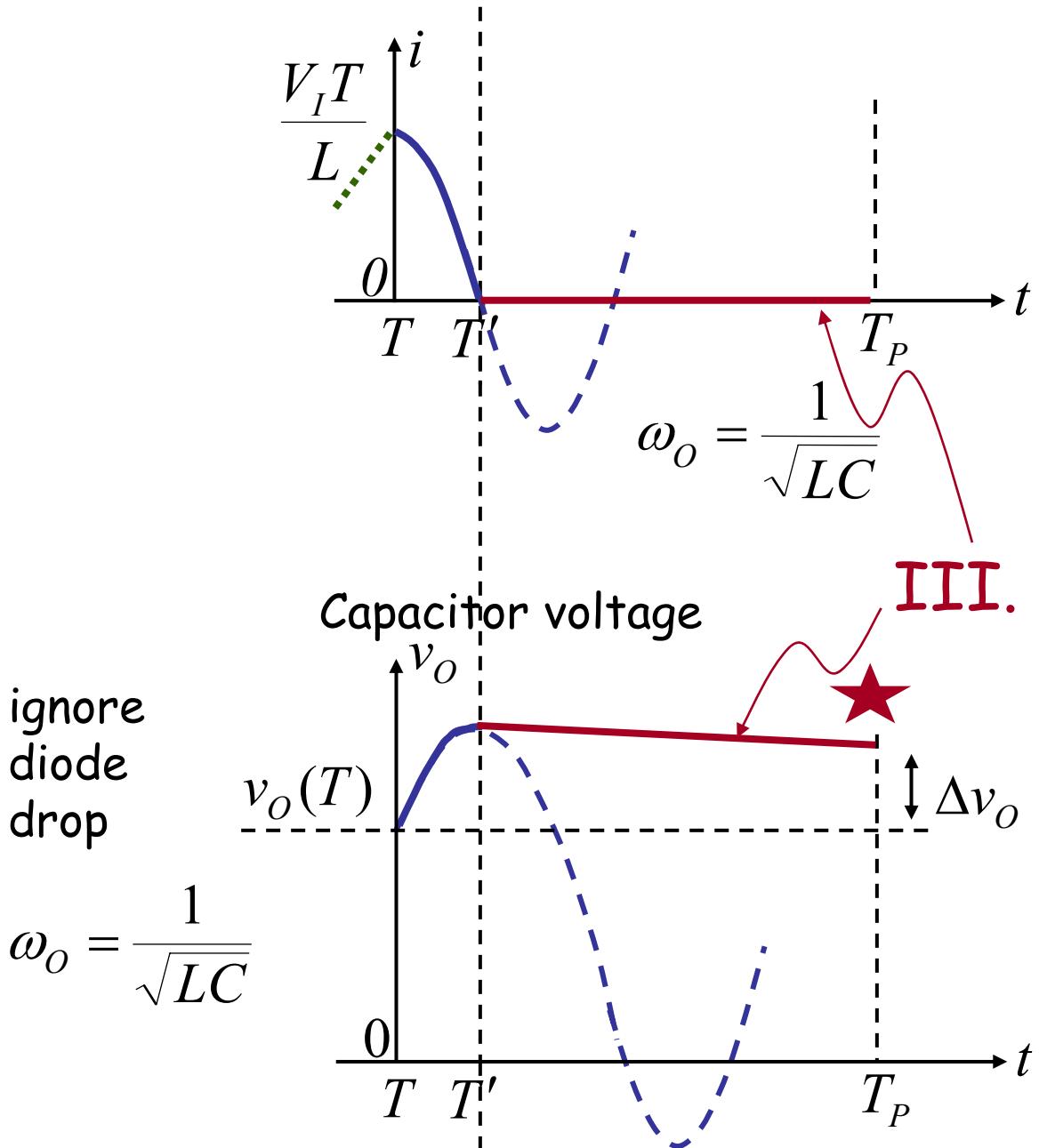
II. S turns off at $t = T$, diode turns on Let's look at the voltage profile



Diode turns off at T' when I tries to go negative.

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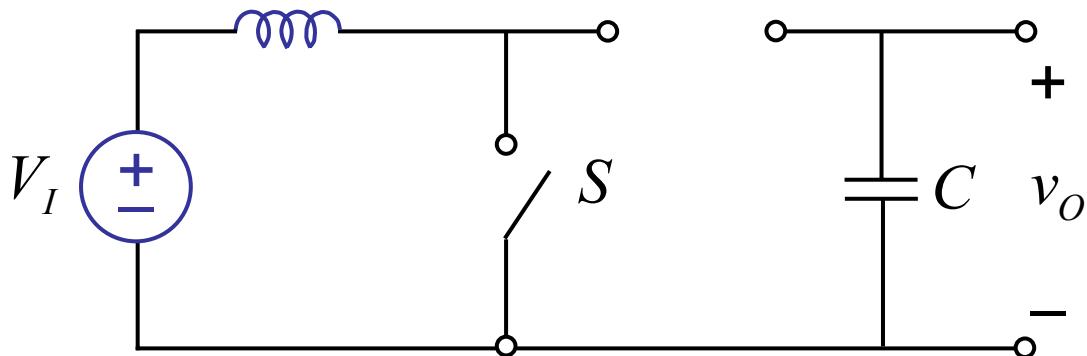


Diode turns off at T' when I tries to go negative.

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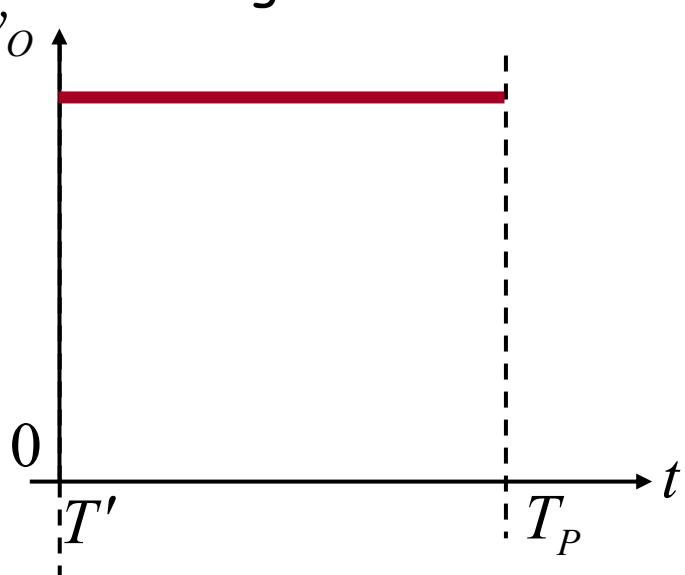
III. S is off, diode turns off

Eg, no load



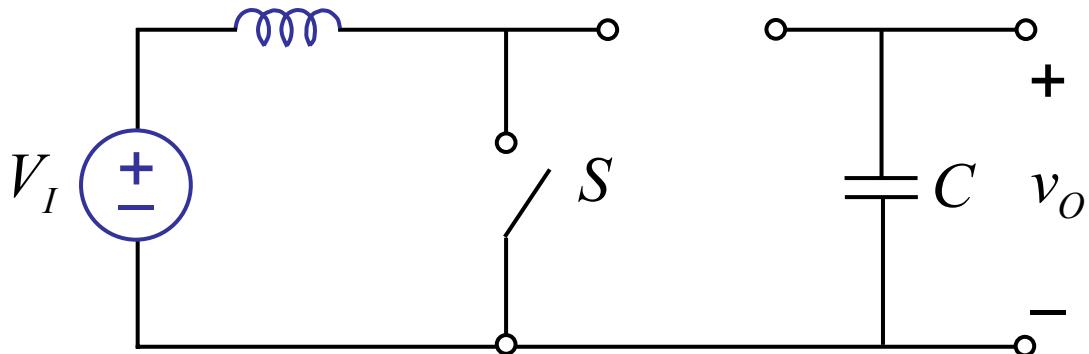
C holds v_O after T'
 i is zero

Capacitor voltage



III. S is off, diode turns off

Eg, no load



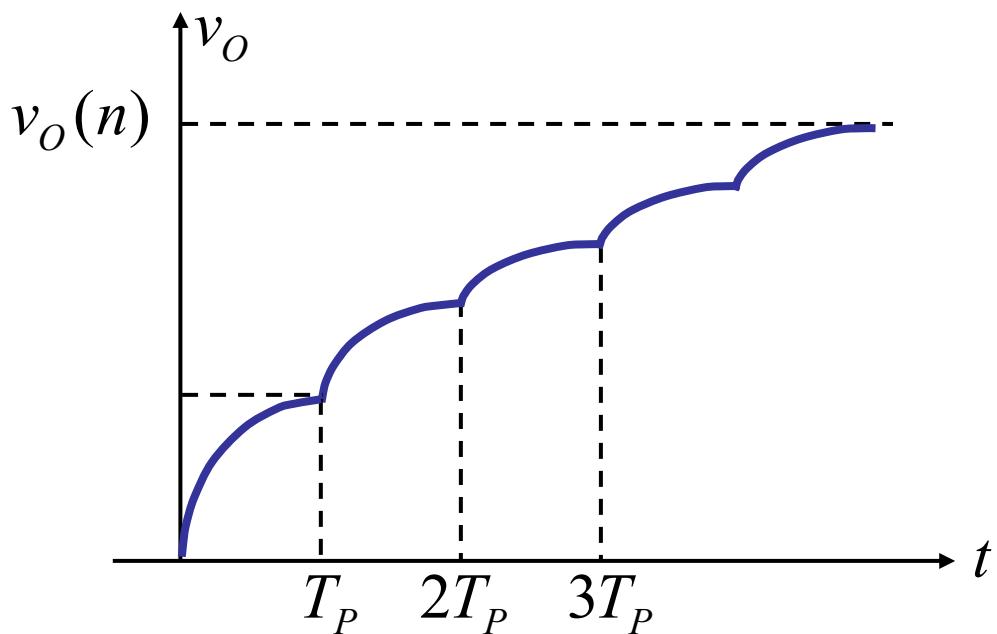
C holds v_O after T'

i is zero

until S turns ON at T_P , and cycle repeats

I II III I II III ...

Thus, v_O increases each cycle, if there is no load.



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What is v_O after n cycles $\rightarrow v_O(n)$?

Use energy argument ... (KVL tedious!)

Each cycle deposits ΔE in capacitor.

$$\Delta E = \frac{1}{2} \frac{V_I^2 T^2}{L} \quad \left\{ \begin{array}{l} \Delta E = \frac{1}{2} L i(t=T)^2 \\ = \frac{1}{2} L \left(\frac{V_I T}{L} \right)^2 \end{array} \right.$$

After n cycles, energy on capacitor

$$n \Delta E = \frac{n V_I^2 T^2}{2L}$$

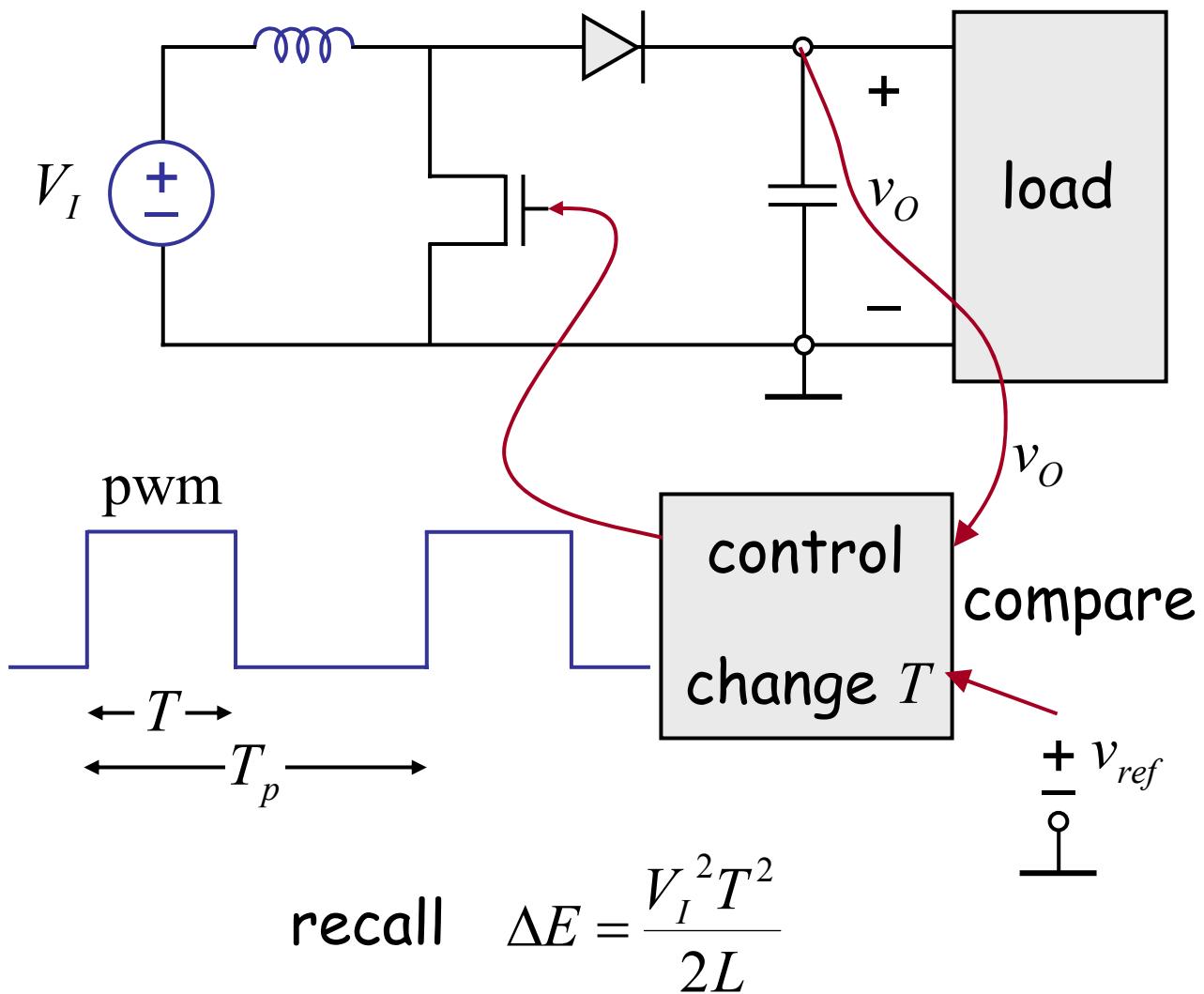
This energy must equal $\frac{1}{2} C v_O(n)^2$

so, $\frac{1}{2} C v_O^2(n) = \frac{n V_I^2 T^2}{2L}$

or $v_O(n) = \sqrt{\frac{n V_I^2 T^2}{LC}} \quad \left\{ \omega_O = \frac{1}{\sqrt{LC}} \right.$

$$v_O(n) = V_I T \omega_O \sqrt{n}$$

How to maintain v_o at a given value?



Another example of negative feedback:

if $(v_o - v_{ref}) \uparrow$ then $T \downarrow$

if $(v_o - v_{ref}) \downarrow$ then $T \uparrow$